**Proposal for the AUN/SEED-Net Regional Conference on Electrical and Electronics Engineering**

**BACKGROUND:**

*The AUN/SEED-Net Regional Conferences*

The Regional Conferences (RC) of the AUN/SEED-Net Program is a regular and active platform for University Researchers, Professors and Graduate Students of different Universities in the ASEAN Region and Japan to share their latest research towards exploring collaboration and cooperation in addressing regional and global issues. In line with enhancing the impact and maximizing the reach of the AUN/SEED-Net Network, the Regional Conferences are expected to engage and foster interaction with external participants, including representatives from the government, industry, community, non-MIs, and other professional organizations. More specifically, the RC aims to achieve the following objectives:

(1) To share recent research trends, the most updated technology and experiences among academicians, industry, community, and policy makers related to a regional common issues;

(2) To publicize research progress and outputs of collaborative research related to a regional common issues;

(3) To review, discuss and design annual implementation plan for AUN/SEED-Net activities in each field and direction of collaborative research on common regional issues; and.

(4) To strengthen the relationships among Member Institutions, Japanese Supporting Universities, industry, community, and policy makers.

**PROPOSAL AND THEME:**

The provisional theme for the RCEEE 2016 is:

*RCEEE-2016: Engineering Innovation toward a Bright Future*

The proposed theme reflects and recognizes the important role of electrical and electronics engineers (EEE) in the development of advanced technology to foster robust growth and sustainable progress in society. Furthermore, it is in keeping with the goal of the AUN/Seed-Net to be able to address common regional issues. In the face of impending regional economic integration, electrical and electronics engineers are the forefront of technology innovation aimed at improving national competitiveness. Amidst increasing challenges due to natural and man-made calamities, electrical and electronics engineers are called upon to be stalwarts of resilient physical and information infrastructure. From electric power systems, renewable energy, mobile and wireless telecommunications, software engineering, computing and cyber security, electronic control, automation and manufacturing to sensor networks and the next generation Internet of Things – the work of the electrical and electronics engineer casts a wide net of influence and impact on societal advancement and its ability to respond to the challenges of improving the quality of life.

On a more specific note, in 2016 Hanoi University of Science and Technology (HUST) will celebrate our 60 years of foundation and if HUST is approved to host the RCEEE-2016, this will become one of our series of events to showcase the development and diversify of our relationship. That is the reason why we choose the dates of conference within the month of our foundation ceremony (October 2016).

*The RCEEE-2016 is tentatively schedule from October 9-10, 2016. The proposed venue will be at Ta Quang Buu library in HUST in Hanoi city, Vietnam.*

The RCEEE-2016 will help provide international exposure to the ongoing research and development activities in various institutions in the Vietnamese and will also make the RCEEE proceedings available to a much bigger audience of scholars, researchers, faculty members, industry and government participants who will come from all over the country. The interaction of academics in the Vietnamese with their peers from other countries will also bring mutual benefit through validation of current research and initiation of possible collaborations among different institutions in Asia Pacific.

For this conference, we will invite several international experts (from Japan, Thailand, The US, etc) to give messages and keynotes on electrical and electronics engineering challenges and solutions for smart and resilient societies.

The Call for Papers will invite researchers, faculty and students to submit their work for consideration in the oral and poster technical sessions. The organizing committee also plans the following activities as part of the conference:

a) Tutorial Workshops

b) Best paper awards

c) Business meeting for AUNSEED/Net Field Management Committee

The target participants are as follows:

A. AUN/SEED-Net Participants

a. Faculty and Students of Member Institutions

B. Vietnam Government Agencies

a. Ministry of Industry and Trade, Vietnam

b. Ministry of Education and Training, Vietnam

c. Ministry of Science and Technology, Vietnam

D. Industry Participants

a. Electricity of Vietnam (EVN)

b. Electric power generation and utility companies

c. Semiconductor and electronics design and manufacturing companies

E. International Researchers

F. Academic and Professional Societies

a. Institute of Energy (IE)

b.

**LIST OF TOPICS:**

The conference technical (oral and poster) sessions will address the following topics:

A. Power System – Electric Machines and Drives; Power Electronics; High Voltage Engineering; Power Distribution; Power Systems; Renewable Energy; Smart Grid; Energy Scavenging and Harvesting; other relevant areas.

B. DEQ and Control; Dynamical Control Systems; Instrumentation other relevant areas.

C. Power Electronics

D. Mesuament

~~Computing – Computer architecture; Computational Intelligence; Software Systems and Engineering; other relevant areas.~~

D. Circuits and Systems – Embedded systems; Analog and Digital Microelectronics; VLSI and SoC; Radio Frequency, Microwave and Milimeter-wave Circuits; Photonics; Bioelectronics; Nanotechnology; Optoelectronics; Antennas and Propagation; other relevant areas.

**ORGANIZING COMMITTEE:**

Hanoi University of Science and Technology:

General Chair:

Assoc. Prof. Dr. Hoang Minh Son,

President, Hanoi University of Science and Technology

General Co-Chairs:

Dr. Nguyen Huy Phuong

Dean, School of Electrical Engineering, HUST

A. Ta Cao Minh

Registration Committee and Secretariat:

Assoc. Prof. Dr. Hoang Si Hong, HUST (*Chair*)

Dr. Tran Tuan Vu, HUST

Publications Committee:

Dr. Le Duc Tung, HUST

Finance Committee:

Assoc. Prof. Dr. Nguyen Thi Lan Huong

Dr. Nguyen Thu Ha, HUST

Local Arrangements Committee:

Dr. Nguyen Xuan Tung, HUST

Dr. Do Thi Tu Anh

Local support staff for operations and logistics:

Dr. Tran Manh Hung

Ms. Tran Thi Kim Hoa

Ms. Nguyen Thi Hoa

Ms. Hoang Thi Kim Chi

Ms. Nguyen Hoang Yen

International Advisory Committee:

Dr. Thavatchai Tayjasanant (Chulalongkorn University, Thailand)

Dr.

Technical Program Committee:

Dr. Le Viet Tien, HUST

Dr. Do Trong Hieu, HUST

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| Area | Committee members |
| Power and Energy | Dr. Bach Quoc Khanh  Dr. Pham Hung Phi |
| Control | Dr. Nguyen Quoc Cuong  Dr. Tran Trong Minh |
| Computing | Dr. Nguyen Doan Phuoc  Dr. Ngo Dinh Hoa |
| Circuits and Systems |  |

**Important dates**

1st Call for papers: April 5, 2016

2nd Call for papers: May 10, 2016

Final Call for papers: June 14, 2016

Abstract submission deadline: July 15, 2016

Acceptance notification: August 31, 2016

Final paper submission: September 14, 2016

Early Bird Registration Deadline: October 2, 2016

Conference Proper: November 9-10, 2016

**Conference language**

English is the official language of the conference.

Papers are to be written and presented orally in English.

**Paper submission**

A 2-page extended abstract should be submitted through email using the template available at the conference website (coming soon). The abstract should be written in English and contain the title, author’s names and their affiliation including email address of the corresponding authors.

Accepted extended abstracts will be printed in the conference proceedings.

**Tentative Conference Program**

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| Time | Day 1 (dd/mm/yy) | Time | Day 2 (dd/mm/yy) |
| 8:00–9:00  9:00–9:30  9:30–10:00  10:00–10:40  10:40–11:00  11:00–11:30  11:30–12:00  12:00–1:30  1:30–3:30  3:30–3:50  3:50–5:30  6:30–8:00 | **Morning:**  *Plenary: Opening + 5 keynotes*  Registration  Opening ceremony  *Welcome remarks*  Messages  Keynote  Coffee break  Invited plenary talk 1 (*Academic*)  Invited plenary talk 2 (*Industry*)  Lunch beak  **Afternoon:**  *8ppt x 4 parallel sections*  Parallel technical sessions  Coffee break  Parallel technical sessions  *Expect to have 64 presentations, each presenting time is 20 minutes (15min for present, 5 min for Q&A)*  (*number of paper may be up to 100*)  **Gala Dinner**  *(Cultural show, Awarding ceremony)* | 9:00–10:30  10:30–10:50  11:00–11:30  11:30–12:00  1:30–5:30 | **Morning:**  Parallel technical sessions  *8ppt x 4 parallel sections*  Coffee Break  Parallel technical sessions  **Closing Ceremony**  **Afternoon:**  Factory Visit |

**Profile of invited speakers (tentative)**

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